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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10007391	10/22/2001	714	733	2184	<i>[Signature]</i>

**APPLICANTS: Nolles Jurgen; Dirscherl Gerd; Gartner Wolfgang;

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KEP VEROS

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF PCT/DE00/01052 04/05/2000

ES ju 2/13/04

** FOREIGN APPLICATIONS VERIFIED:

GERMANY 199 17 884.4 04/20/1999

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

ju 2/13/04 9999P1681P

TITLE : Circuit including a built-in self-test

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner			
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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